

DETECTION of MINIMUM IONISING PARTICLES with CMOS SENSORS

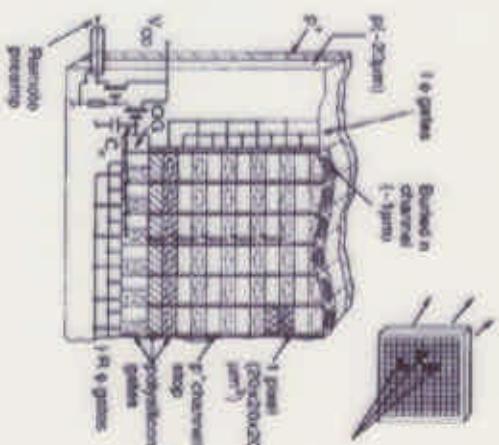
M.Winter (IReS-Strasbourg)
on behalf of the IReS-LEPSI collaboration

Outline:

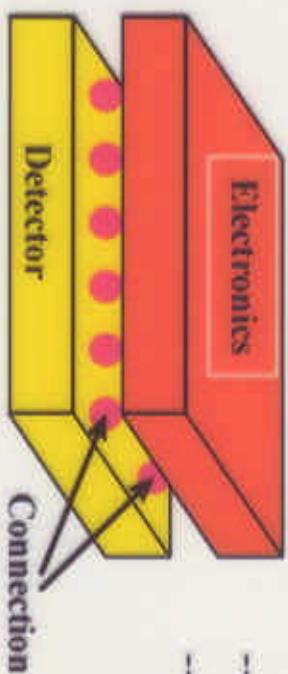
- ◆ **Introductory Remarks**
- ◆ **Main Features of the First Prototype**
- ◆ **Simulation of Performances**
- ◆ **Beam Test Results (still preliminary)**
- ◆ **Summary and Outlook**

PIXEL DETECTORS

CCD



hybride APS



APS (Active Pixel Sensor)

High Resistivity monolithic APS

--> High Energy Physics
--> Complex technology



--> High Energy Physics
(DELPHI, ...)

Low Resistivity monolithic APS

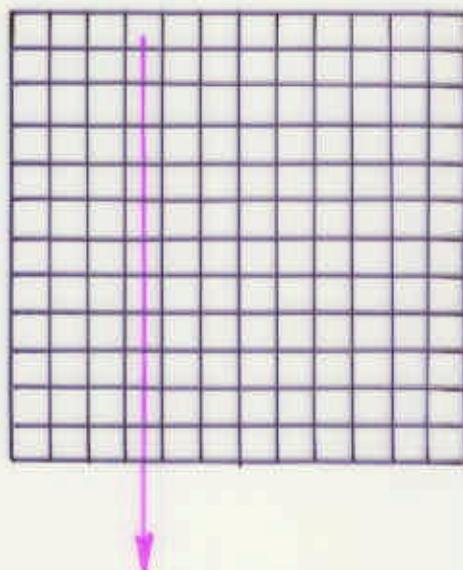
--> Imaging
--> Standard VLSI Technology

--> Imaging
--> High Energy Physics
(SLD)

■ Existing Technics

► CCD

- ◆ Specific Limits:
 - radiation hardness
 - quite slow ($\sim 10 - 100 \mu s$)



**read-out column wise
by charge transfer**

► High resistivity hybride APS

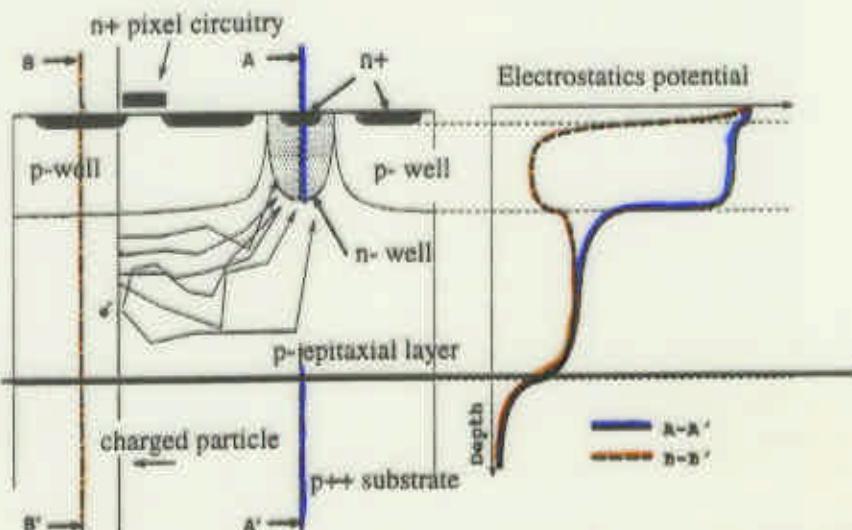
- ◆ Specific limits:
 - thickness (200 to 400 μm)
 - minimal size of pixels ($> 50 \times 50 \mu m^2$)

► Weakness of both technics —→ Cost !!! :

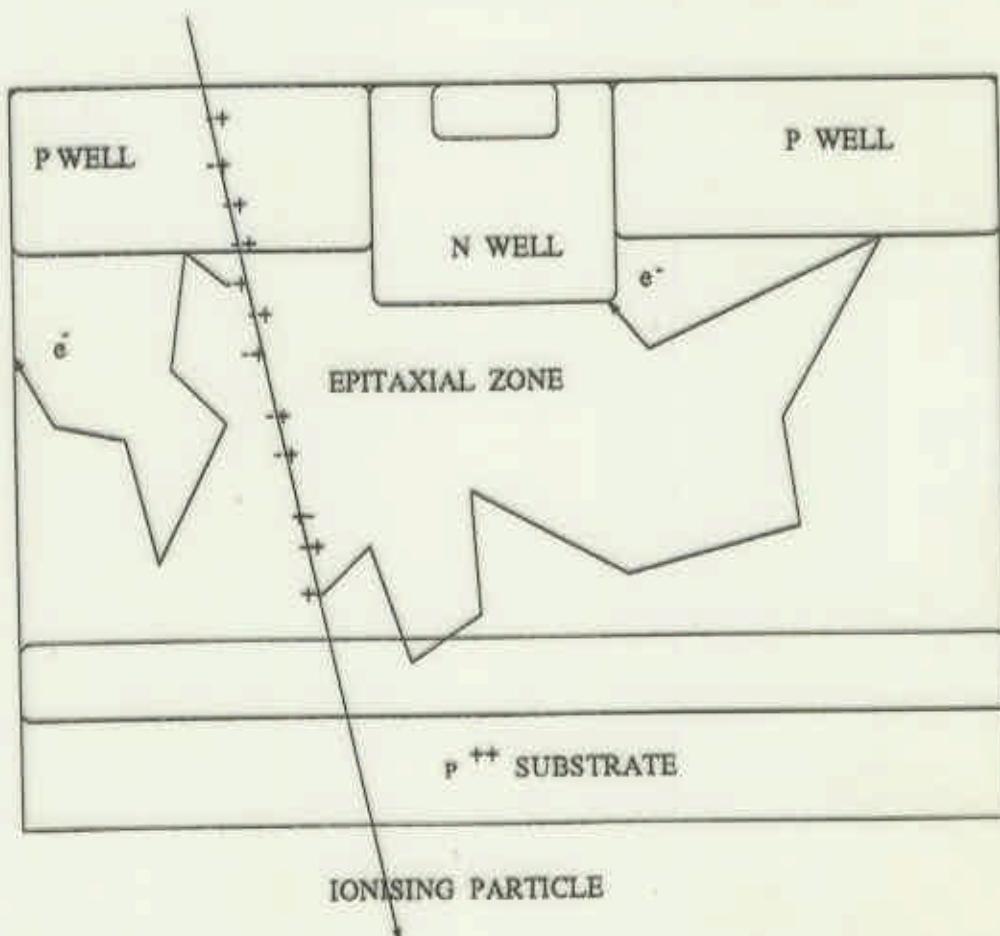
$> 100 \text{ CHF} / \text{cm}^2 \Rightarrow$ Vertex Det. cost several MCHF

■ Principle of Operation of CMOS Sensors (2)

• Internal Structure of Pixel



→ no external drift field (no bias potential)



- Signal produced in epitaxial layer
- ❓ Does the substrate contribute ???

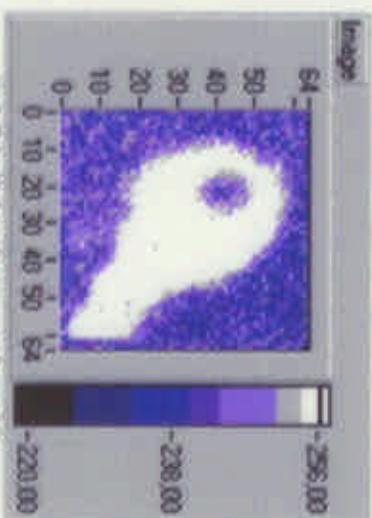
MIMOSA I (Minimum Ionising particle MOS Active pixel detector)

Goal of fabrication:

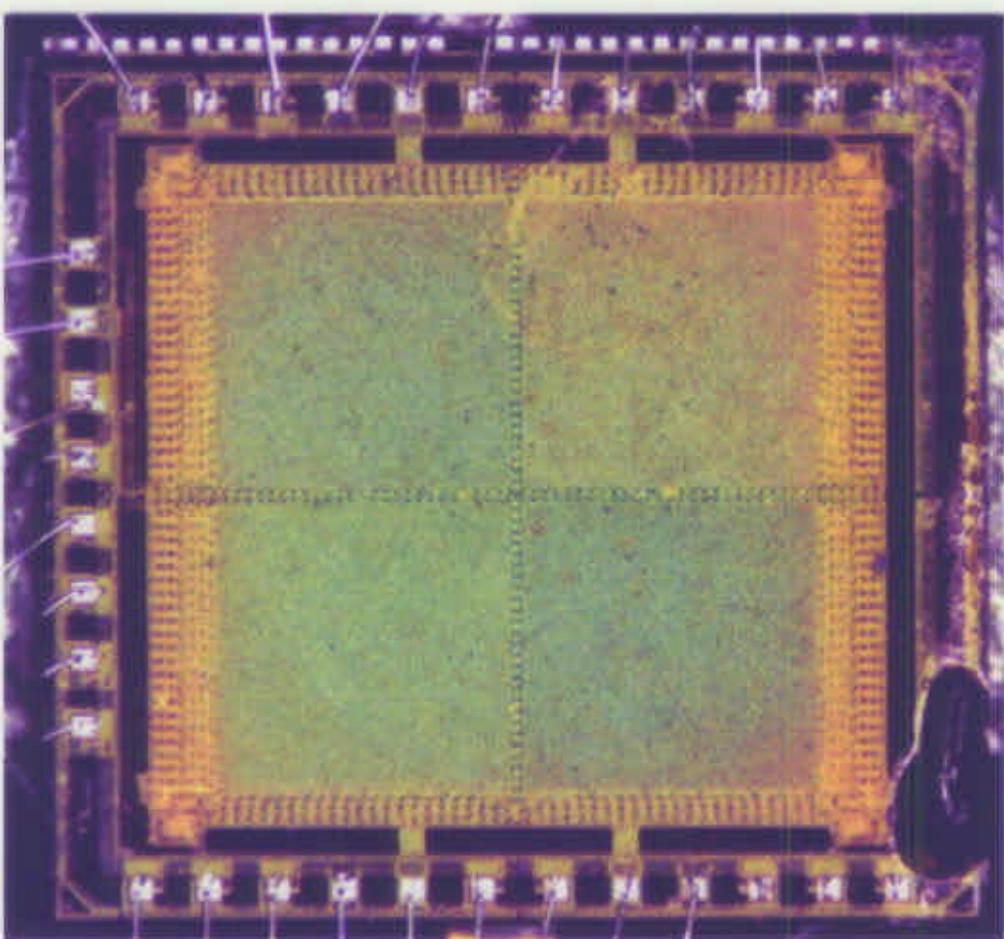
- feasibility study
- understanding/tests

• standard $0.6\mu\text{m}$ CMOS ($t_{ox}=12.7\text{nm}$)

- $14\mu\text{m}$ thick EPI layer (10^{14}cm^{-3})
- 4 arrays 64×64 pixels
- pixel pitch $20\times 20\mu\text{m}$
- diode (n -well/ p -epi) size $3\times 3\mu\text{m}$ - $3.1fF$
- readout clock $f < 10\text{MHz}$
- readout - serial analog
- die size $3.6\times 4.2\text{mm}^2$

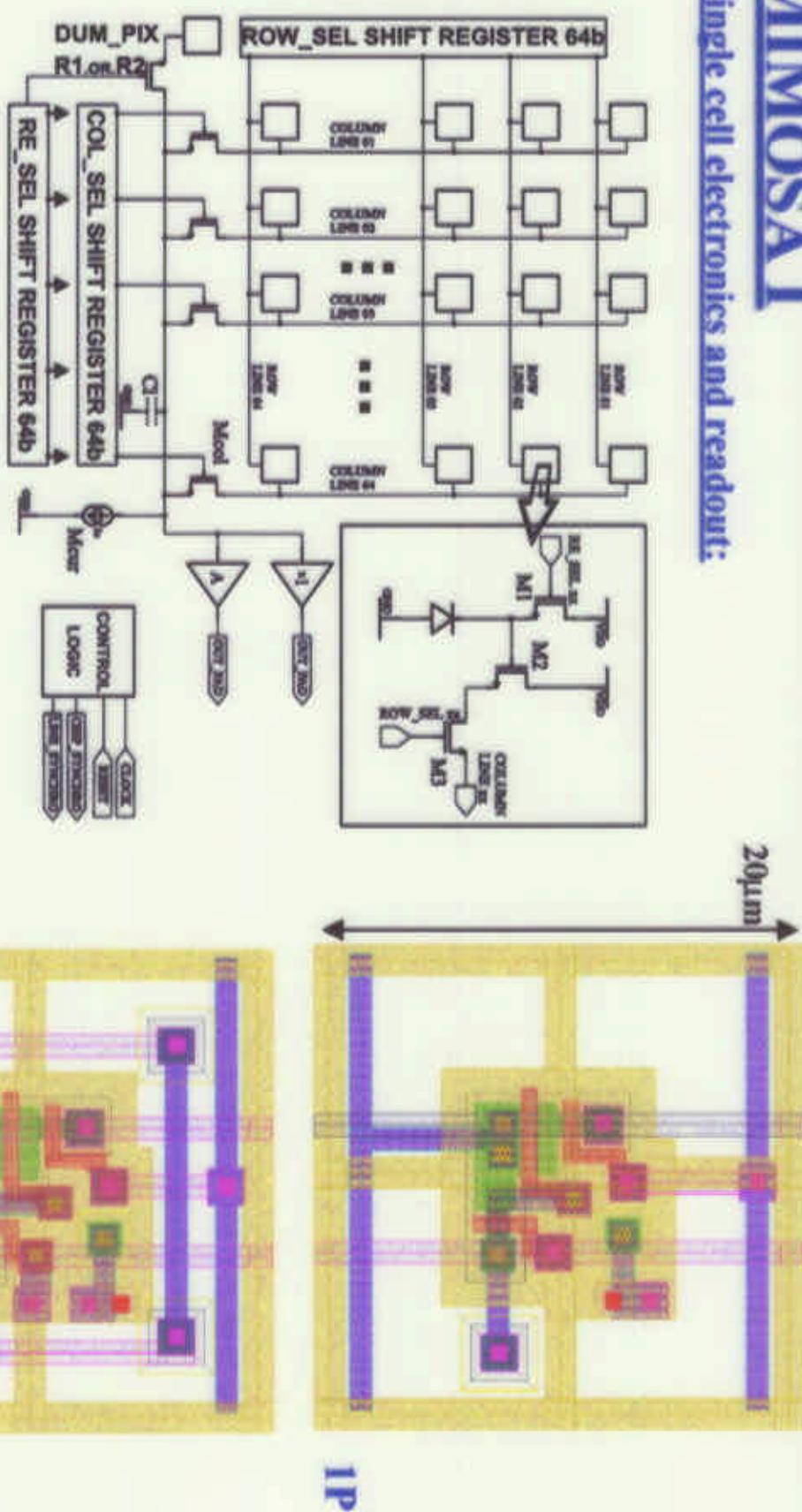


"visible light photography with
MIMOSA"



MIMOSA I

Single cell electronics and readout:



- work regime:
... RESET ... READOUT ... RESET...

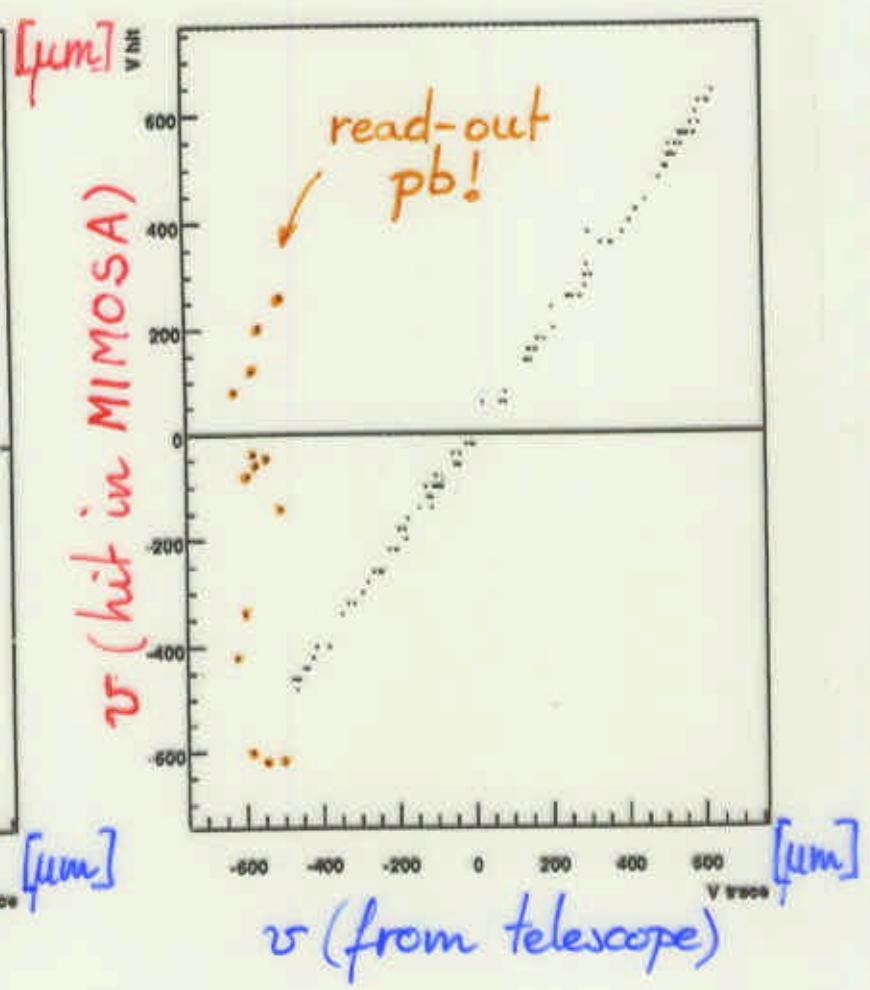
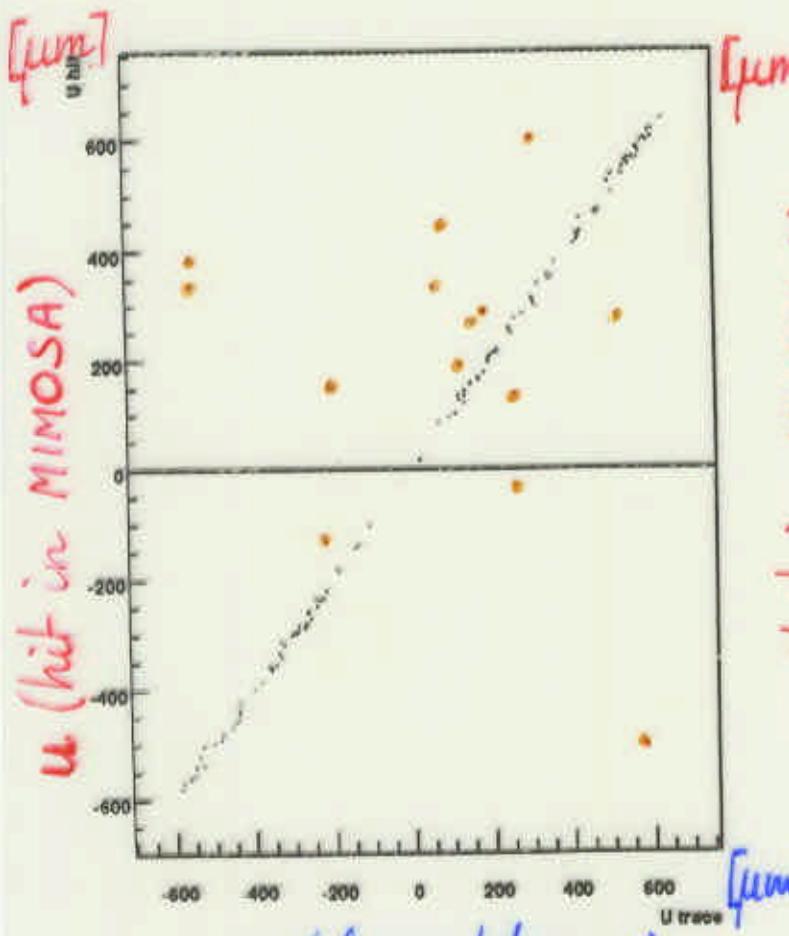
- technology 3M+2P
- power supply 4÷5V

Beam Tests: 15 GeV/c // (CERN)
 $\sim 5 \cdot 10^4$ triggers

► MIMOSA-I mounted on Si-strip Telescope

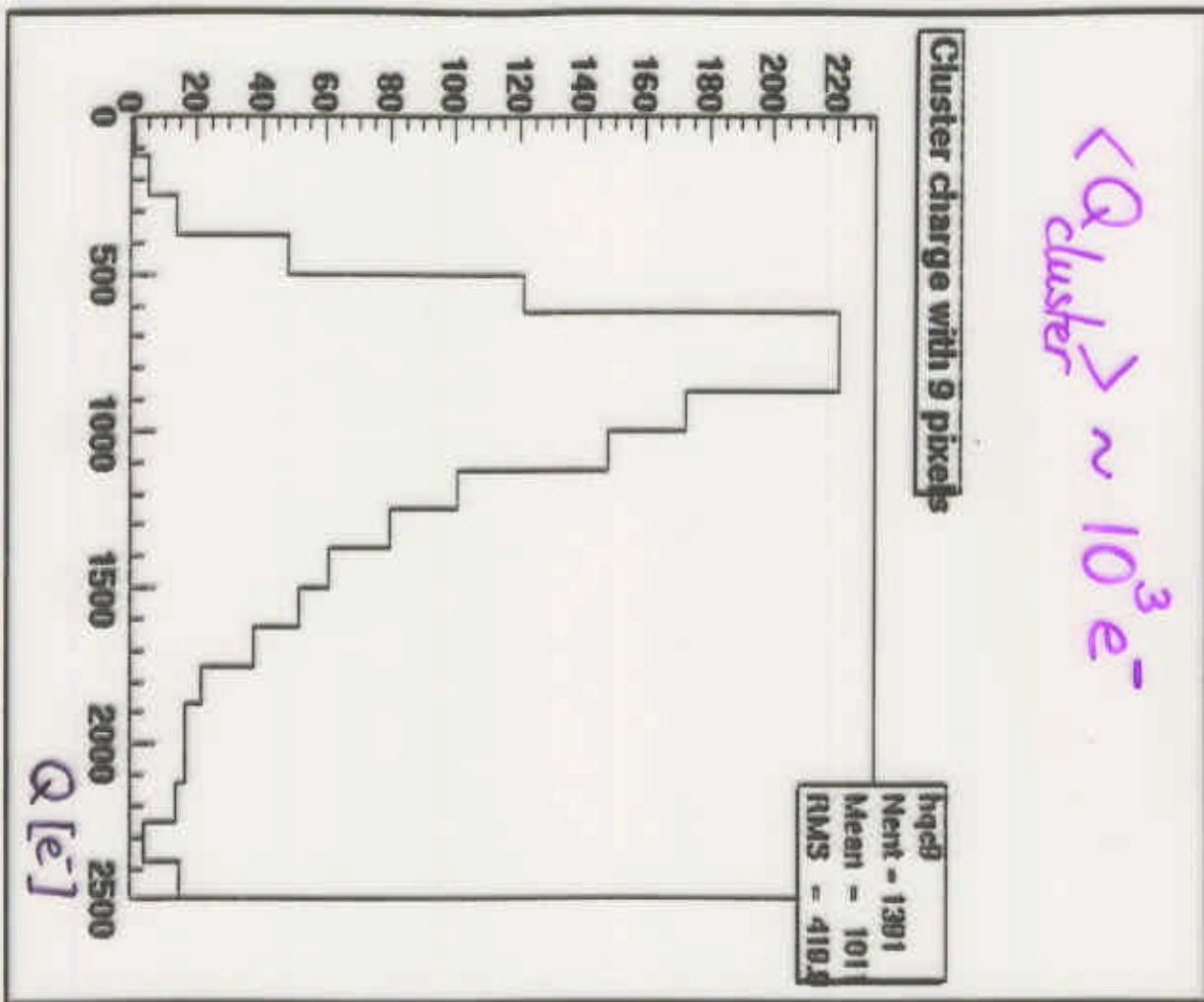


► Correlation between tracks in telescope
and hits in MIMOSA-I

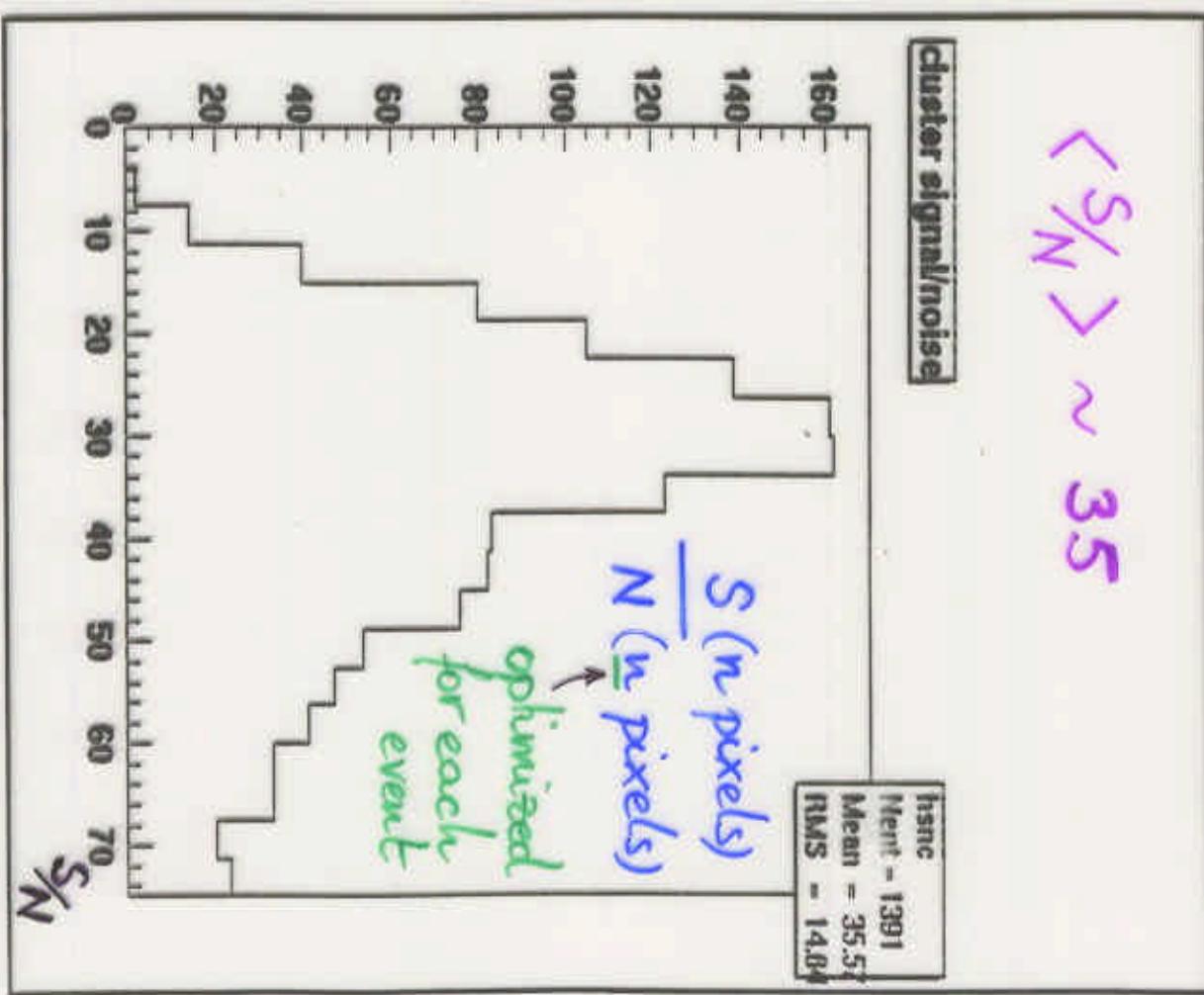


$$\epsilon_{\text{det.}} \geq 99.5 \pm 0.2 \%$$

$\langle Q_{\text{cluster}} \rangle \sim 10^3 e^-$



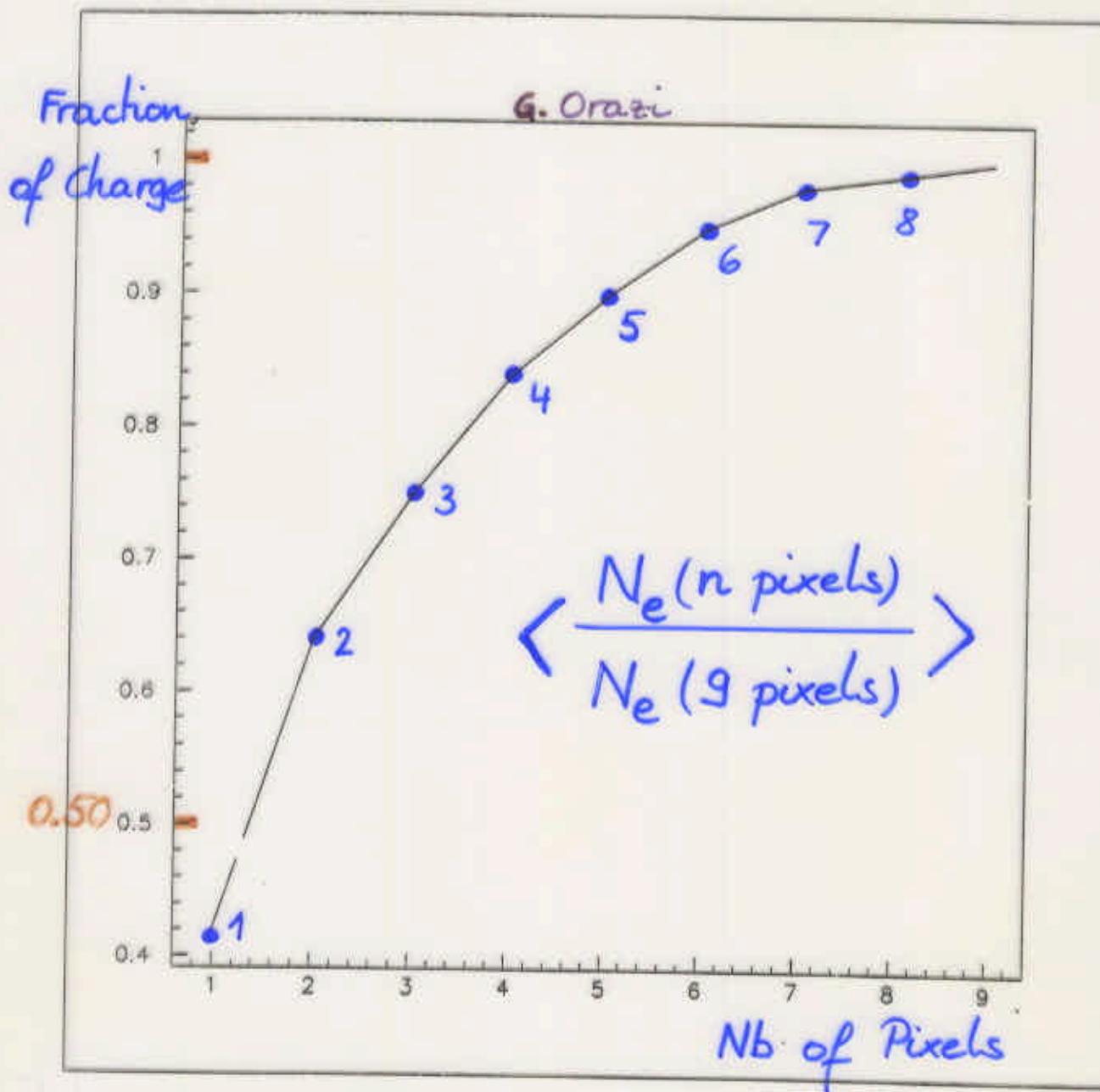
$\langle S/N \rangle \sim 35$



calibrated with ^{55}Fe X-ray (5.9 keV)

■ Charge Spread over Pixels (1)

39

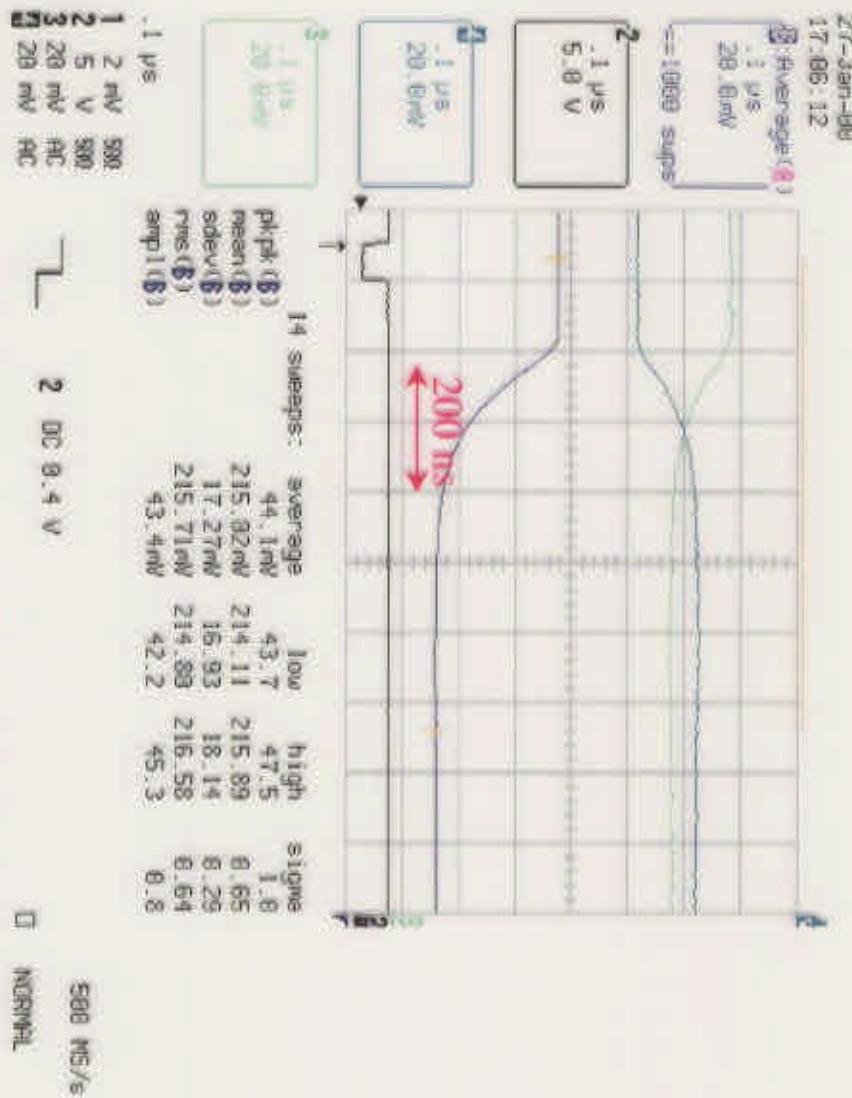


~ 40% of charge in central pixel

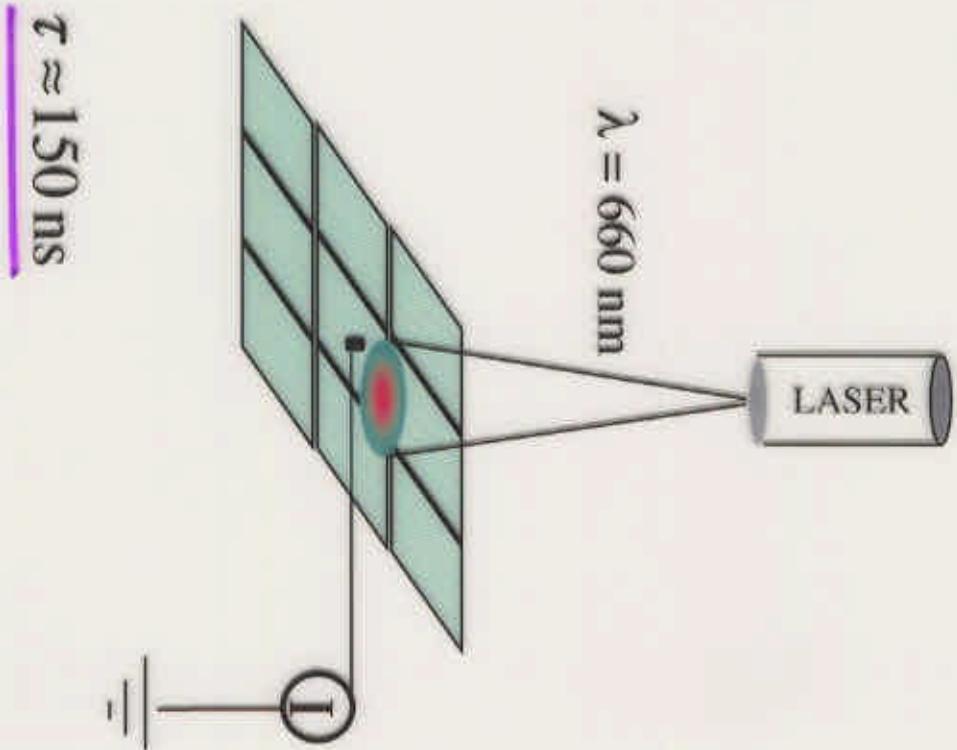
~ 65-75% of charge contained
in first 2 or 3 pixels

Measurement of MIMOSA-I's charge collection Time

→ simulate m.i.p. with laser shot



$$\lambda = 660 \text{ nm}$$



■ SUMMARY and OUTLOOK

► Detection of m.i.p. with CMOS sensors WORKS!

$$\langle S/N \rangle \sim 30 - 35 \quad \epsilon > 99.5\%$$

→ Performances already similar to those of CCDs
combined with better radiation hardness (> 10 krad)

► Next Steps:

1 • additional studies and tests of MIMOSA-I:

- determine spatial resolution, increase statistics, ...
- test radiation hardness (neutrons, protons, ...)
- test reaction to magnetic field and low temperature

2 • test MIMOSA-II → several improvements:

- radiation hard design
- optimised S/N ($0.35 \mu m$ ⇒ thinner epi-layer)
- hexagonal geometry ⇒ better geometrical precision

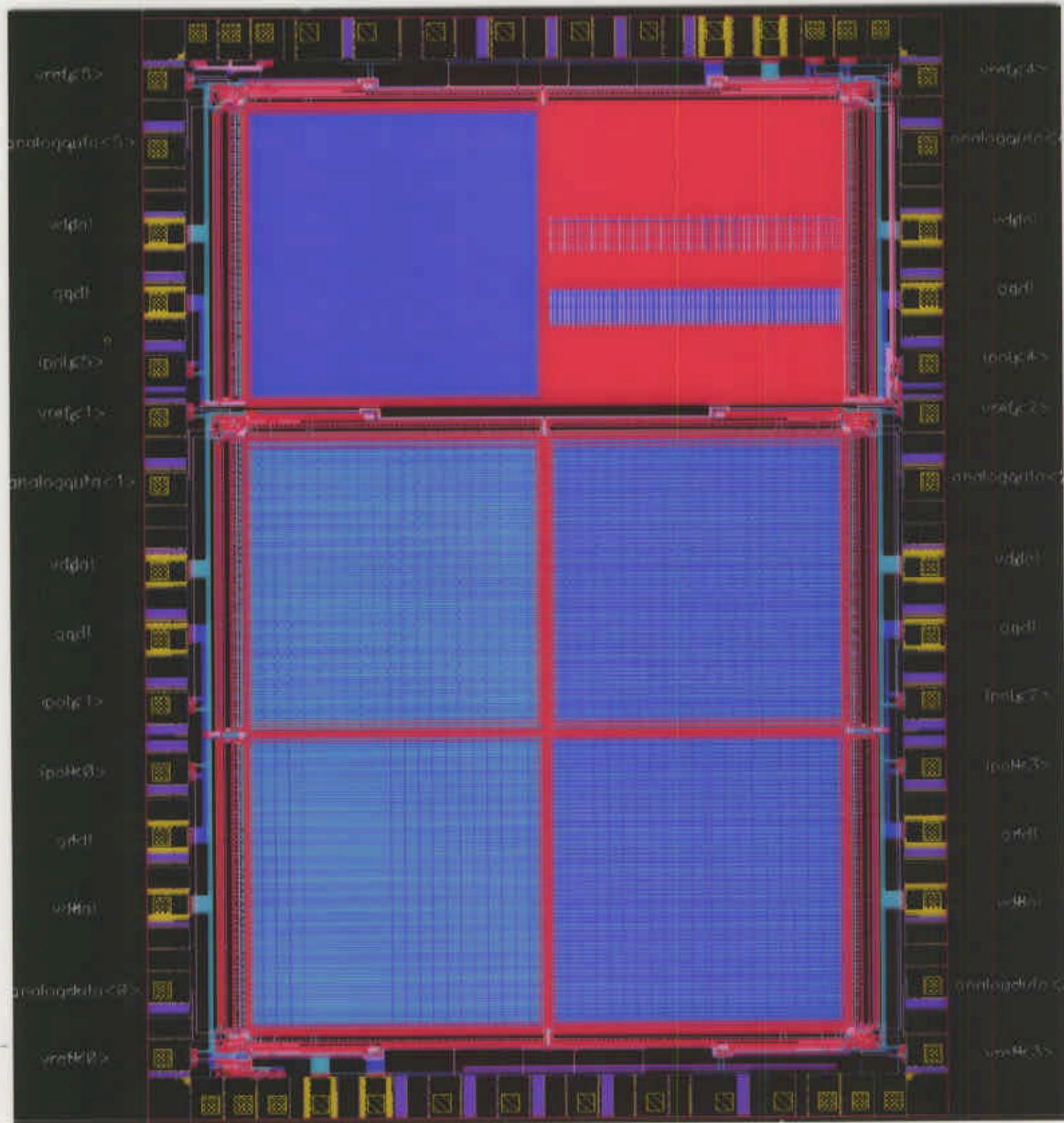
Fig. 

3 • make a macroscopic detector module ($> 10 \text{ cm}^2$)

Fig. 

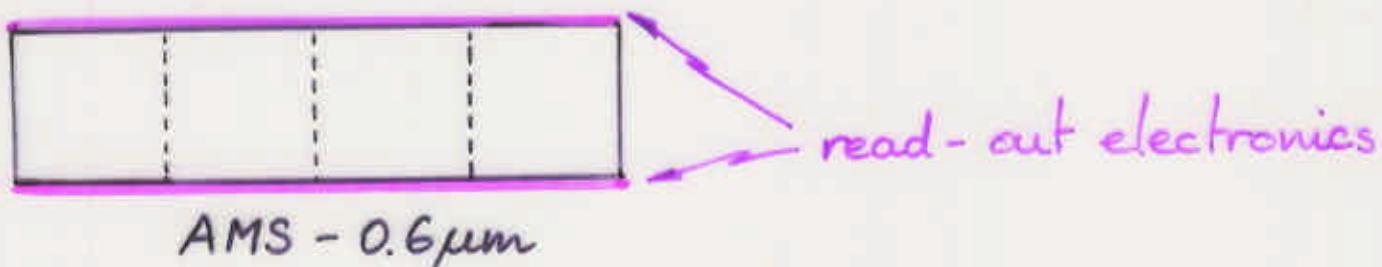
4 • go for an intelligent and fast sensor:

- integrate decision electronics in each pixel
- develop read-out electronics with 0 suppression



First CMOS Macrostructure

aim for $15 \times 45-60 \text{ mm}^2$ slab (= MIMOSA-I)



optimise read-out frequency

→ send layout in Sept. 2000 → back in December

- pixel size : $\sim 50 \times 100 \mu\text{m}^2$ (aim of BELLE)?
⇒ $32 \cdot 10^3$ pixels ($\approx 128 \times 256$) / module
read with 30 MHz frequency / frame
 - tot. thickness / layer $\sim 150 \mu\text{m}$
 - geometry :
- | | | |
|-----|-----|-----|
| ... | ... | ... |
| ... | ... | ... |
| ... | ... | ... |
- 3 diodes / pixel?